Voltage Smoothing with a Capacitor

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Abstract
A rectifier is an electrical circuit that converts alternating current to direct current, e.g., a half wave rectifier removes lower valleys of the AC voltage wave. If a capacitor is placed in parallel with a half wave rectifier, the transition from voltage peak to peak is smoothed to closer resemble constant DC voltage. The objective of this project was to model the voltage changes of a half wave rectifier with a parallel capacitor. It was found that the smoothness of the voltage waves increased as the capacitance of the capacitor increased.

Keywords
AC/DC Converter, Rectifier, Voltage smoothing, Parallel Capacitor

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PROBLEM STATEMENT

A half wave rectifier converts AC current to DC current – which is now unidirectional but still varies with time, as shown in the Figure 1. For particular applications, it may be important that the variation in voltage with time be much smaller than that of the rectified wave.

![Figure 1: Half wave rectified signal.](image)

One way to smooth the half wave rectified voltage is to place a capacitor in parallel with the load, as shown in the circuit below where $R_L$ represents the resistance of the load:

![Figure 2: Circuit for smooth half wave rectifier.](image)

Without the capacitor, the load voltage $v_L$ would look like the bottom graph of Figure 1. By adding the capacitor, the load voltage $v_L$ is smoothed and looks as Figure 3:
The goal of this project is to quantify the behavior of the load voltage $v_L$ for a given capacitance $C$ and to examine the degree to which the capacitance can smooth the load voltage. The behavior of the circuit is governed by the following equations. When the diode passes current (for $\theta_1 < \theta < \theta_2$), the load voltage is the same as the source voltage:

$$v_L = V_m \sin \theta$$  \hspace{1cm} (1)

where $\theta = \omega t$ ($\omega$ is the frequency of the voltage source) and $V_m$ is the maximum voltage.

When the diode does not pass current (for $\theta_2 < \theta < \theta_3$), the behavior of the circuit is governed by:

$$R_L C \frac{dv_L}{dx} + v_L = 0$$  \hspace{1cm} (2)

where $R_L$ is the resistance of the load in $\Omega$ (Ohms) and $C$ is the capacitance in $F$ (Farads). Note that $\theta_3 = \theta_1 + 2\pi$ and $\theta_2 = \frac{\pi}{2}$. Suppose that the frequency of the voltage source is 377 rad/s (corresponding to 60 cycles/sec) and the load resistance is 4,000 $\Omega$. Plot the load voltage $v_L$ versus $\theta$ for two different cases – one in which the capacitance is $50 \times 10^{-6} F$ and one for which it is $5 \times 10^{-6} F$. 

Figure 3: Voltage and current waveforms
MOTIVATION

Alternating current (AC) can be converted into direct current (DC) by using a half wave rectifier. However, after converting to direct current, only the half of the original signal is passed to the next stage, giving a large variation of voltage with time (ripples). Most electronic applications required DC voltage with a smoother change in voltage. In order to smooth these changes (ripples), a capacitor is placed in parallel with the load voltage. The motivation of this project is to find the dependence of the capacitance value on the smoothing process.

MATHEMATICAL DESCRIPTION AND SOLUTION APPROACH

Graphical approach was selected to solve the given problem. First, the given differential equation for $v_L$ for the range $\theta_2 < \theta < \theta_3$ integrated to find an equation of $v_L$ between $\theta_2$ and $\theta_3$.

$$R_L C \frac{dv_L}{dt} + v_L = 0 \quad (3)$$

Simplifying (3) to separate and integrating both sides gives,

$$\int \frac{dv_L}{v_L} = - \int \frac{dt}{R_L C} \quad (4)$$

i.e.,

$$\ln|v_L| = - \frac{t}{R_L C} + \ln k \quad (5)$$

where $k$ is some unknown constant. Solving for $v_L$ for $\theta_2 < \theta < \theta_3$:
\( v_L = k e^{\left( \frac{-t}{R_L C} \right)} \).

(6)

The frequency of the voltage source is given as 377 rad/sec (corresponding to 60 cycles/sec).

The time and \( \theta \) are related by,

\[ \theta = \omega t \]

(7)

and, considering both formulas for the voltage, we note that at \( \theta = \frac{\pi}{2} \), where both curves given in (1) and (2) intersects. At \( \theta = \frac{\pi}{2} \), equation (1) becomes \( v_L = V_m \), equation (7) becomes \( t = \frac{\theta}{\omega} = \frac{\pi}{2 \omega} \), and solving for \( k \) in (6) gives

\[ k = V_m \exp \left\{ \frac{\pi}{2 \omega R_L} \right\}. \]

(8)

and in general over the interval \([\theta_1, \theta_3]\):

\[ v_L = \begin{cases} 
V_m \sin \theta, & 0 < \theta_1 \leq \theta < \theta_2 = \frac{\pi}{2}; \\
V_m \exp \left(\frac{\pi - \theta}{\omega R_L C}\right), & \frac{\pi}{2} = \theta_2 \leq \theta < \theta_3 \leq \frac{5\pi}{2}. 
\end{cases} \]

(9)

Recall that \( \theta_1 = \theta_3 - 2\pi \), but it remains to compute \( \theta_3 \). However \( \theta_3 \) is the solution to

\[ V_m \sin \theta = V_m \exp \left(\frac{\pi - \theta}{\omega R_L C}\right) \]

(10)

for some \( \frac{\pi}{2} < \theta \leq \frac{5\pi}{2} \). Taking \( \omega = 377 \text{ rad/s} \), \( R_L = 4,000 \Omega \), \( C = 50 \times 10^{-6} \text{ s} \), and \( V_m = 1 \text{ V} \), Excel computes that \( \theta_3 \approx 7.463748 \) (see Figure 4). It should be noted that this solution does not depend on \( V_m \) as it can be divided through on both sides of (10).
This single period can be generalized from (9) to describe the entire solution as:

\[ v_L = \begin{cases} 
V_m \exp\left(\frac{\pi - \theta}{\omega R_L C}\right), & 0 \leq \theta \pmod{2\pi} < \theta_1; \\
V_m \sin \theta, & \theta_1 \leq \theta \pmod{2\pi} < \frac{\pi}{2}; \\
V_m \exp\left(\frac{\pi - \theta}{\omega R_L C}\right) \left(\frac{\pi}{2} \leq \theta \pmod{2\pi} < 2\pi;\right)
\end{cases} \tag{11}
\]

where \( \theta_1 \approx 1.1806 \) (see Figure 5).
If instead the capacitance $C$ is decreased to $5 \times 10^{-6} F$, the voltage across the load can again be calculated using the same method as before to find that $\theta_1 \approx 0.5229$ (see Figure 6).

![Graph of voltage $v_L$ and $\sin \theta$ vs. $\theta$](https://scholarcommons.usf.edu/ujmm/vol5/iss2/2)

**Figure 6**: Load voltage $v_L$ with $C = 5 \times 10^{-6} F$ and the sine wave.

After examining both graphs, we were able to determine that as the capacitance of the parallel capacitor increases, the degree of smoothing of the graph increases. This means we need to use higher capacitance value for parallel capacitor in order to obtain better smoothed voltage.

**DISCUSSION**

We were able to meet the objectives of the project, and we managed to not only quantify the behavior of the voltage when a capacitor is added to the circuit, but also to calculate the average load voltage for each case. Our results show that the bigger the capacitance of the parallel capacitor added, the smoother the voltage will become.

**CONCLUSION AND RECOMMENDATIONS**

This project helped us study the effect of capacitors on rectifier circuits. Voltage ripple can be smoothed by adding parallel capacitors across the load in a rectifier circuit. The degree of smoothness increases when the capacitance increases.
NOMENCLATURE

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Magnitude</th>
<th>Units</th>
</tr>
</thead>
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<tr>
<td>$\omega$</td>
<td>Frequency</td>
<td>Radians per Seconds (rad/s)</td>
</tr>
<tr>
<td>$R_L$</td>
<td>Load Resistance</td>
<td>Ohms (Ω)</td>
</tr>
<tr>
<td>$C$</td>
<td>Capacitance</td>
<td>Farads (F)</td>
</tr>
<tr>
<td>$V_L$</td>
<td>Load Voltage</td>
<td>Volts (V)</td>
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REFERENCES


